

Electronic Acknowledgement Receipt

EFS ID:	4326369
Application Number:	10732868
International Application Number:	
Confirmation Number:	6860
Title of Invention:	CIRCUITS AND METHODS FOR PROVIDING VARIABLE DATA I/O WIDTH FOR SEMICONDUCTOR MEMORY DEVICES
First Named Inventor/Applicant Name:	Han-Gu Sohn
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Attorney Docket Number:	8729-226JHM/SS20062US
Receipt Date:	20-NOV-2008
Filing Date:	10-DEC-2003
Time Stamp:	15:51:33
Application Type:	Utility under 35 USC 111(a)

Payment information:

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed (SB/08)	IDS-8729-226.pdf	98927 1454128118118668b3c30474b727a6b2b 00a5f4	no	3
Warnings:					
Information:					
This is not an USPTO supplied IDS fillable form					
2	Foreign Reference	ref02-JP61000992A-8729-226. pdf	127401 af64388f1f1be812028f14d655c43c38744b 5800	no	5
Warnings:					
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3	NPL Documents	ref03-NPL-DEOA-8729-226.pdf	177349 ed548a7295a6c7795edc82f9032748a99077 98b11	no	4
Warnings:					
Information:					
4	Foreign Reference	ref01- KR20020032689A-8729-226.pdf	490525 4b3c37846bdad047815daf66dc10efac0f8 60f	no	13
Warnings:					
Information:					
5	Fee Worksheet (PTO-06)	fee-info.pdf	30569 7e43854a4ac9d90e88f175c34532ae866e e0d4	no	2
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